

30V N-ch Power MOSFET

General Features

- Proprietary New Trench Technology
- $R_{DS(ON),typ.}=1.3m\Omega@V_{GS}=10V$
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

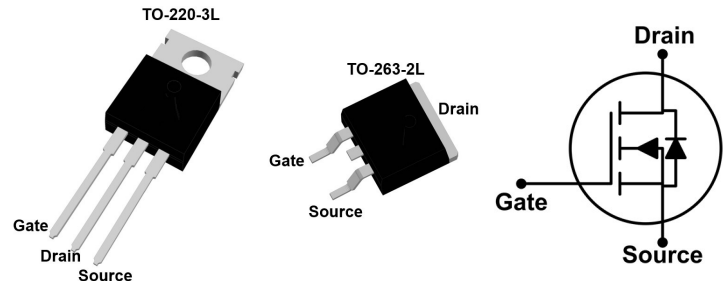
Applications

- High efficiency DC/DC Converters
- Synchronous Rectification
- UPS Inverter

Ordering Information

Part Number	Package	Marking
FTP30N1P6L	TO-220-3L	30N1P6L
FTB30N1P6L	TO-263-2L	30N1P6L

BV_{DSS}	$R_{DS(ON),max.}$	$I_D^{[2]}$
30V	1.6m Ω	279A



Absolute Maximum Ratings

$T_C=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Value	Unit
V_{DSS}	Drain-to-Source Voltage ^[1]	30	V
V_{GSS}	Gate-to-Source Voltage	± 20	
I_D	Continuous Drain Current ^[2]	279	A
	Continuous Drain Current ^[3]	192	
	Continuous Drain Current at $T_C=100^{\circ}C$ ^[2]	197	
I_{DM}	Pulsed Drain Current at $V_{GS}=10V$ ^[2,4]	1114	
E_{AS}	Single Pulse Avalanche Energy ($V_{DD}=15V, V_{GS}=10V, R_G=25\Omega, L=1mH$)	338	mJ
P_D	Power Dissipation	221	W
	Derating Factor above 25 $^{\circ}C$	1.5	W/ $^{\circ}C$
T_L	Soldering Temperature	300	$^{\circ}C$
	Distance of 1.6mm from case for 10 seconds		
T_J & T_{STG}	Operating and Storage Temperature Range	-55 to 175	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case			0.68	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient			63	

Electrical Characteristics

OFF Characteristics

 $T_J=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	30			V	$V_{GS}=0V, I_D=250\mu A$
I_{DSS}	Drain-to-Source Leakage Current			1	μA	$V_{DS}=24V, V_{GS}=0V$
I_{GSS}	Gate-to-Source Leakage Current			± 100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$

ON Characteristics

 $T_J=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$R_{DS(ON)}$	Static Drain-to-Source On-Resistance	--	1.3	1.6	m Ω	$V_{GS}=10V, I_D=80A^{[5]}$
		--	1.6	2.2	m Ω	$V_{GS}=4.5V, I_D=80A^{[5]}$
$V_{GS(TH)}$	Gate Threshold Voltage	1.0	--	3.0	V	$V_{DS}=V_{GS}, I_D=250\mu A$

Dynamic Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
C_{iss}	Input Capacitance		5.0		nF	$V_{GS}=0V, V_{DS}=25V, f=1.0MHz$
C_{rss}	Reverse Transfer Capacitance		0.56			
C_{oss}	Output Capacitance		1.1			
R_g	Gate Series Resistance		1.3		Ω	$f=1.0MHz$
Q_g	Total Gate Charge		68		nC	$V_{DD}=15V, I_D=80A, V_{GS}=4.5V$
			123			
Q_{gs}	Gate-to-Source Charge		12			$V_{DD}=15V, I_D=80A, V_{GS}=10V$
Q_{gd}	Gate-to-Drain (Miller) Charge		39			

Resistive Switching Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$t_{d(on)}$	Turn-on Delay Time		927		ns	$V_{DD}=15V, I_D=80A, V_{GS}=10V, R_G=2.5\Omega$
t_{rise}	Rise Time		16			
$t_{d(off)}$	Turn-off Delay Time		260			
t_{fall}	Fall Time		26			

Source-Drain Body Diode Characteristics

 $T_J=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
I_{SD}	Continuous Source Current ^[2]			279	A	Maximum Ratings
V_{SD}	Diode Forward Voltage		0.9	1.2	V	$I_S=80A, V_{GS}=0V$
t_{rr}	Reverse Recovery Time		102		ns	$V_{GS}=0V, I_F=20A, di/dt=100A/\mu s$
Q_{rr}	Reverse Recovery Charge		180		nC	

Note:

 [1] $T_J=25^{\circ}\text{C}$ to 175°C

[2] Silicon limited current only

[3] Package limited current

[4] Repetitive rating, pulse width limited by maximum junction temperature.

 [5] Pulse width $\leq 380\mu s$; duty cycle $\leq 2\%$.

Typical Characteristics

Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case

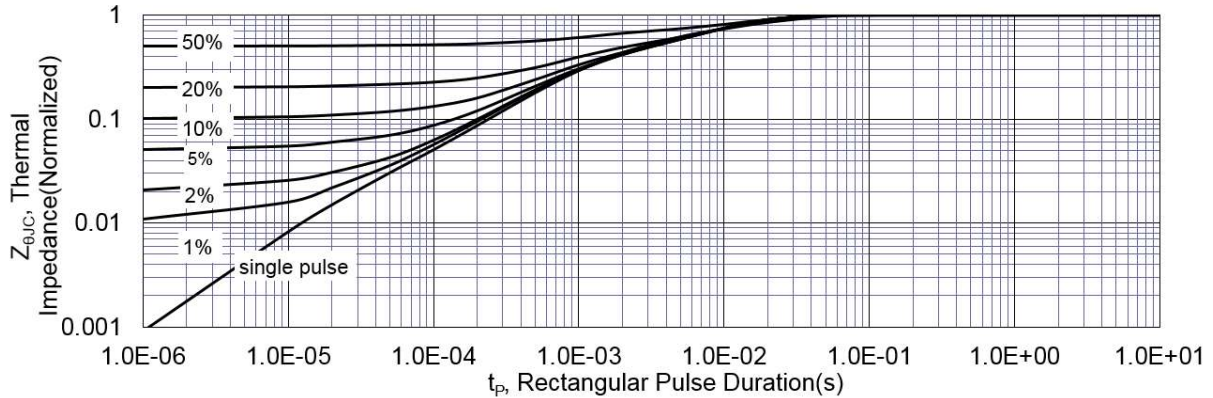


Figure 2. Maximum Power Dissipation vs. Case Temperature

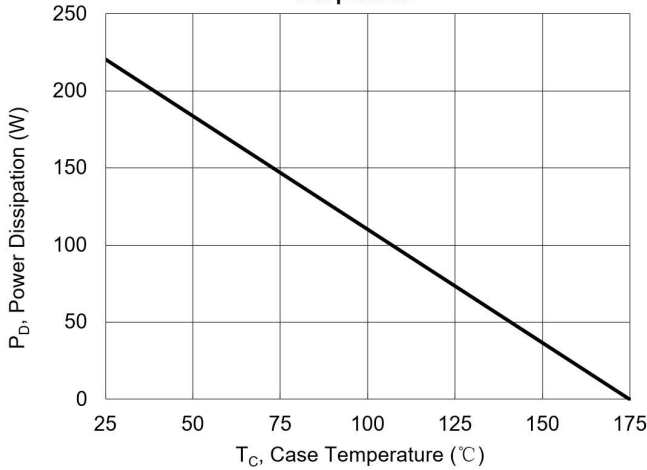


Figure 3. Maximum Continuous Drain Current vs Case Temperature

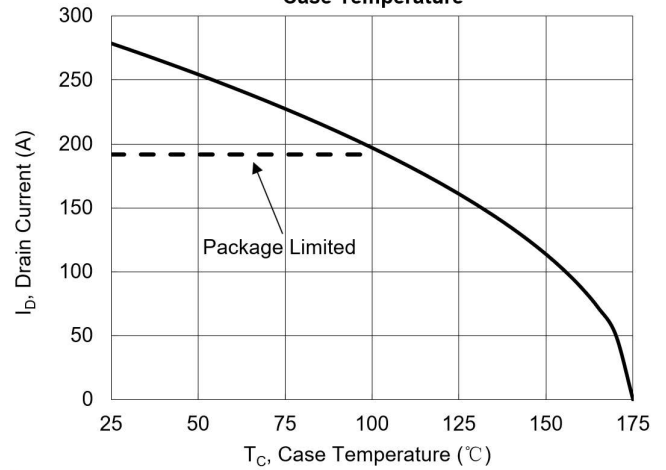


Figure 4. Typical Output Characteristics

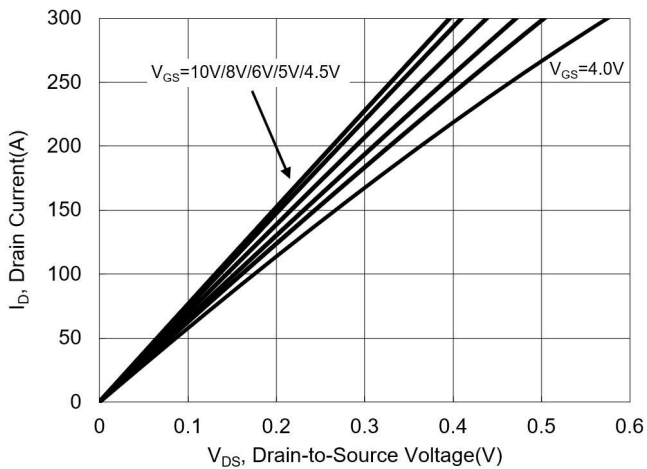


Figure 5. Typical Drain-to-Source ON Resistance vs. Gate Voltage

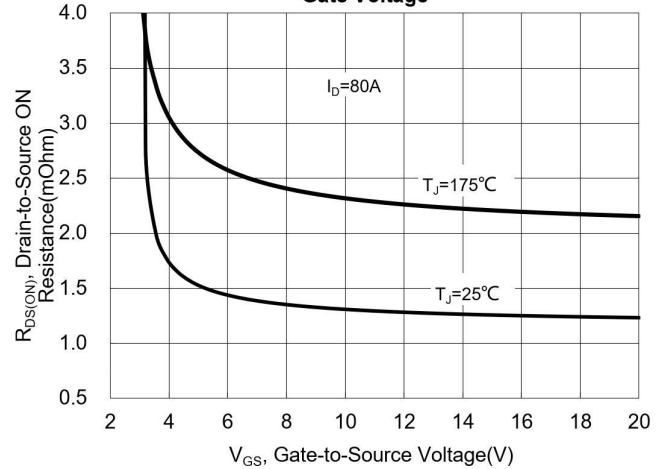


Figure 6. Maximum Peak Current Capability

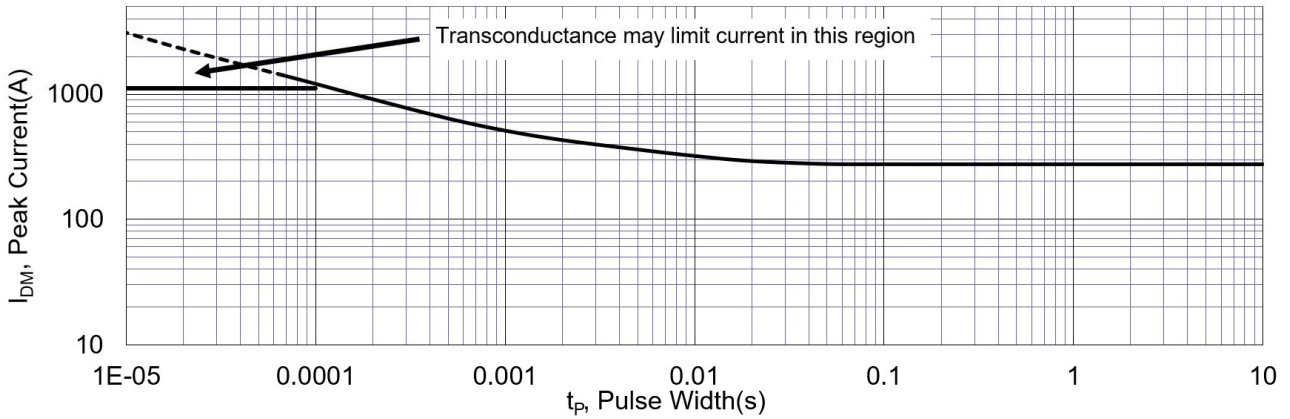


Figure 7. Typical Transfer Characteristics

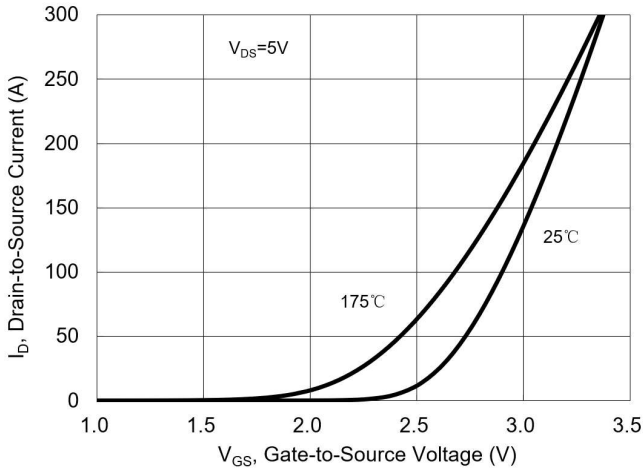


Figure 8. Unclamped Inductive Switching Capability

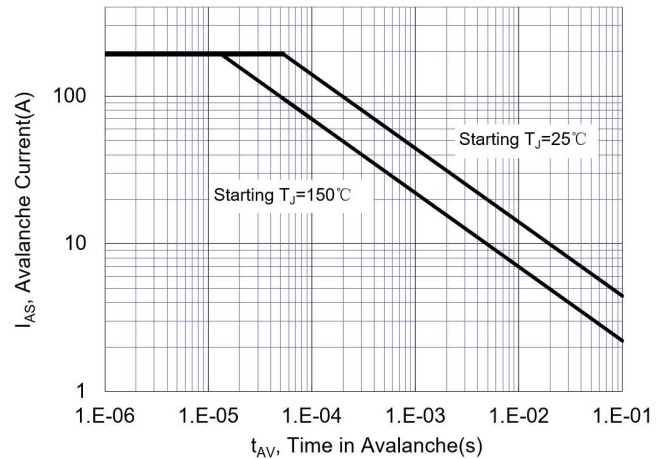


Figure 9. Typical Drain-to-Source ON Resistance

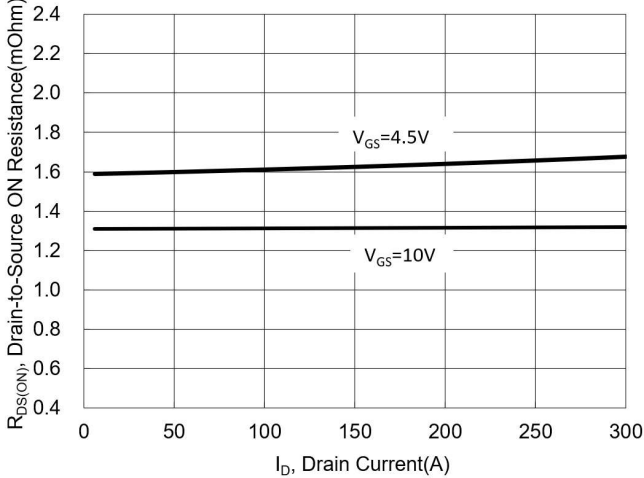


Figure 10. Typical Drain-to-Source On Resistance vs. Junction Temperature

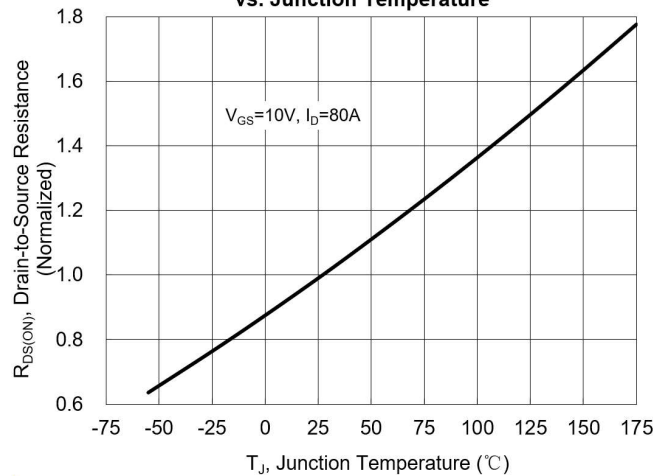


Figure 11. Typical Breakdown Voltage vs. Junction Temperature

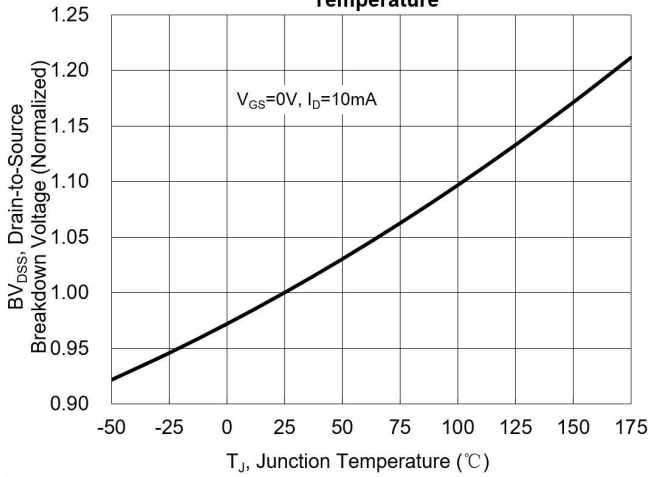


Figure 12. Typical Threshold Voltage vs. Junction Temperature

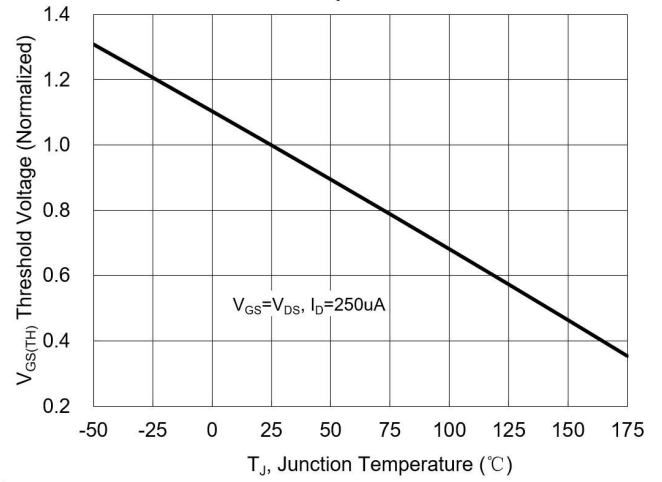


Figure 13. Maximum Forward Safe Operation Area

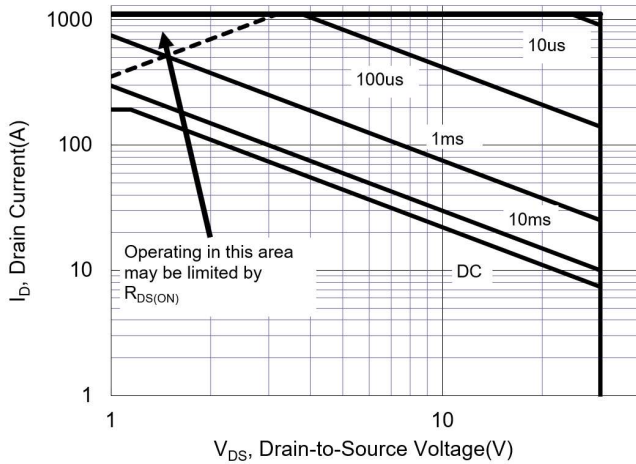


Figure 14. Typical Capacitance vs. Drain-to-Source Voltage

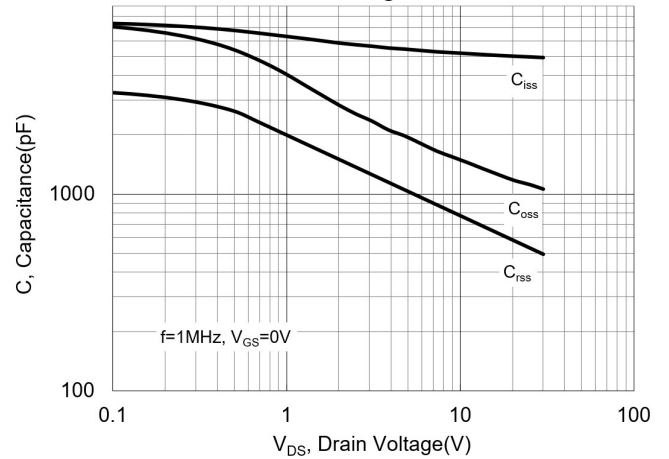


Figure 15. Typical Gate Charge vs. Gate-to-Source Voltage

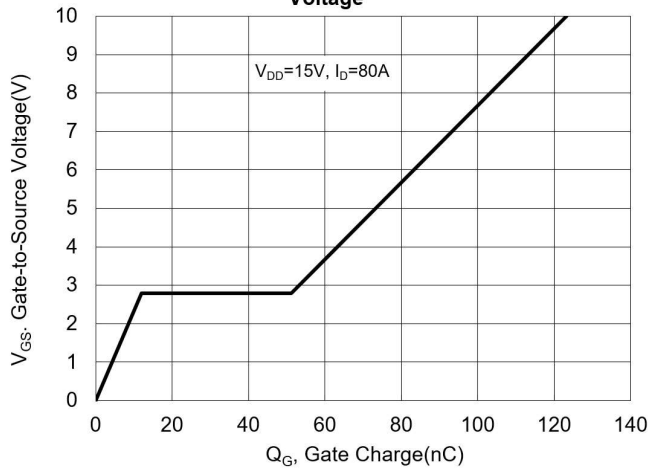
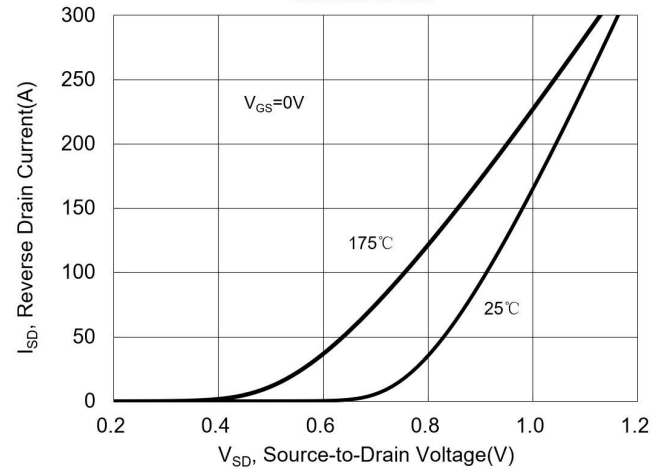
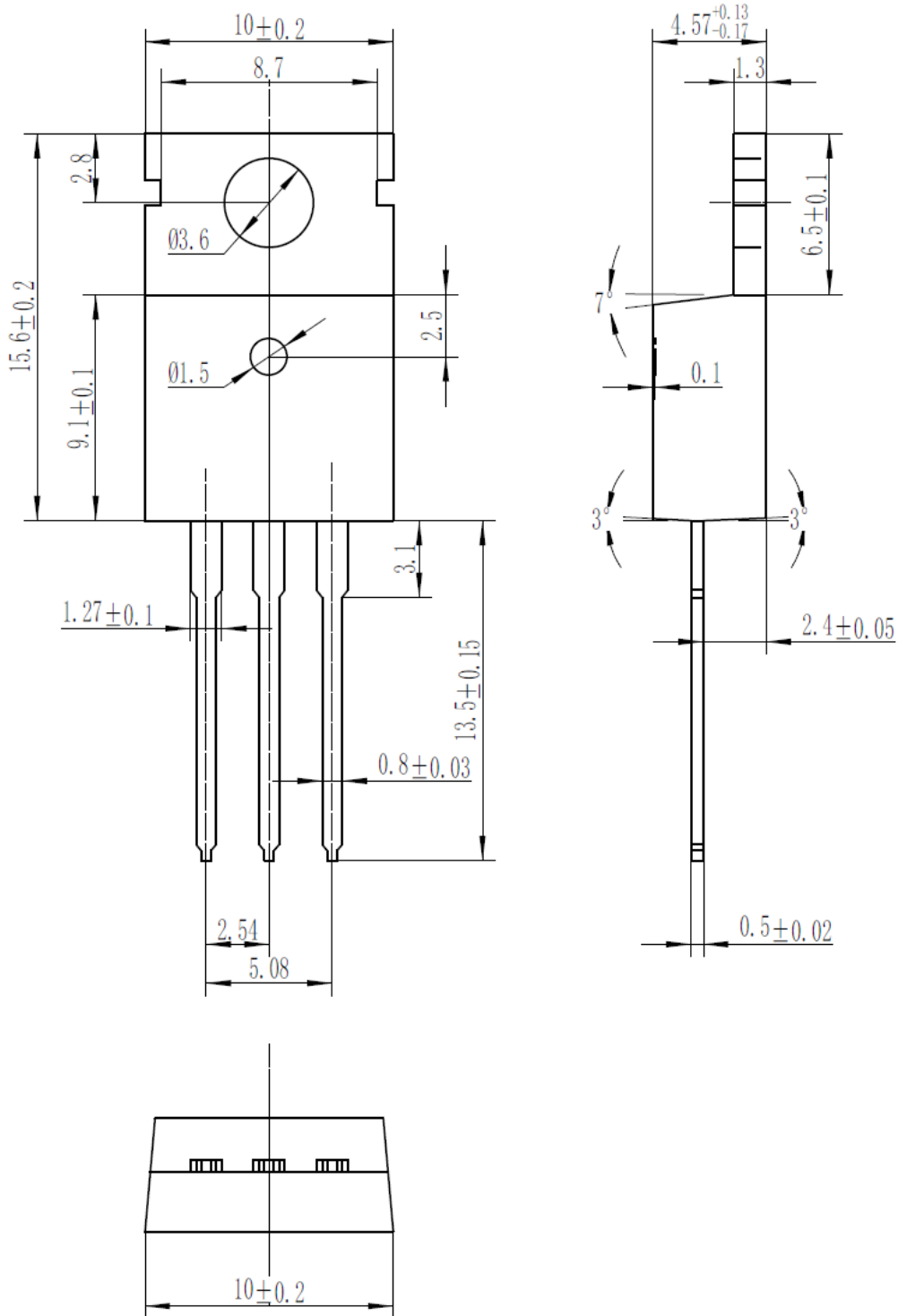


Figure 16. Typical Body Diode Transfer Characteristics

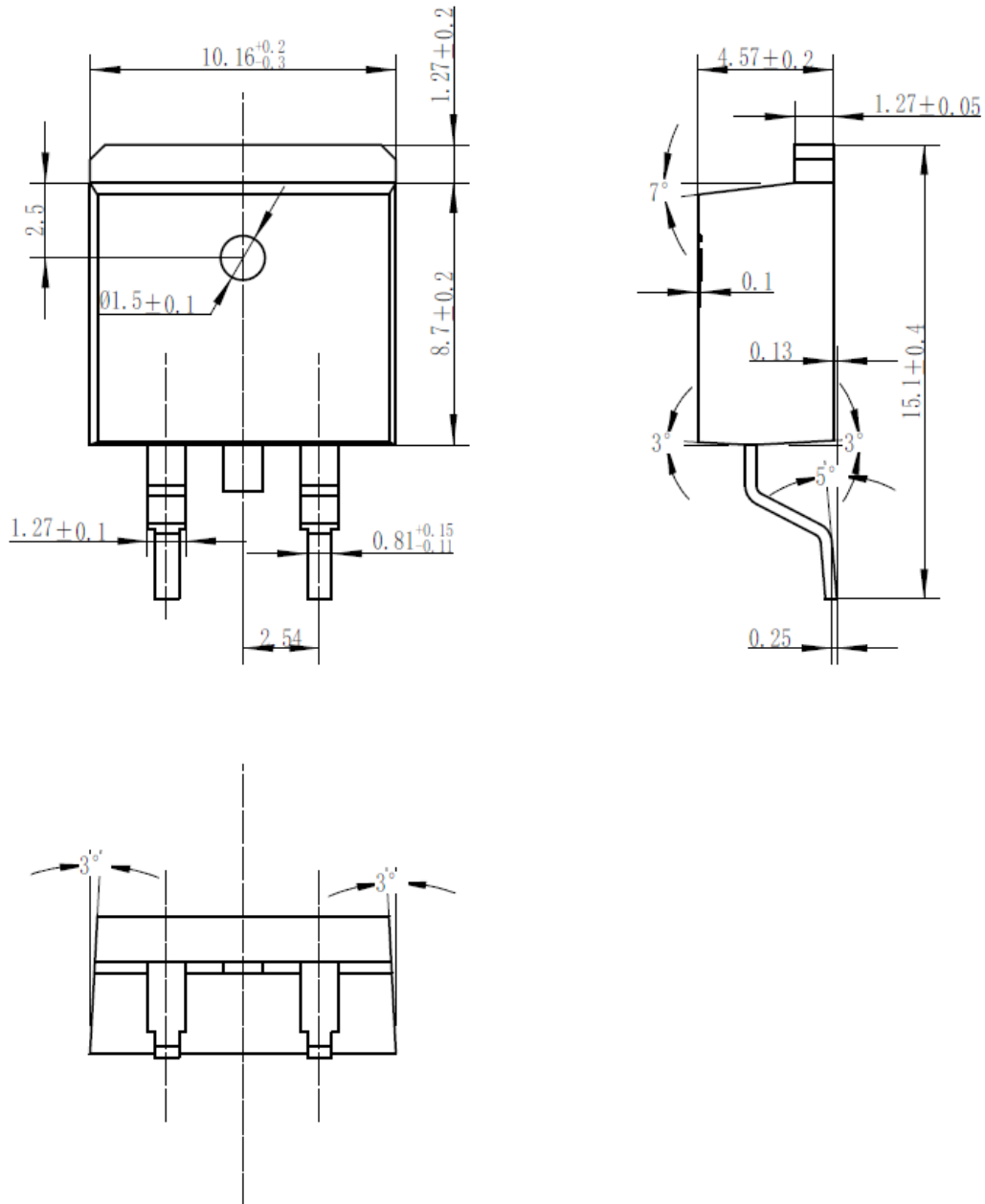


Package Dimensions

TO-220-3L



TO-263-2L



Published by**ARK Microelectronics Co., Ltd.****ADD: 4F,D26,UESTC National Science Park No. 1 Shuangxing Avenue, Gongxing Street ,Shuangliu District, Chengdu, China (Sichuan) Pilot Free Trade Zone.****Disclaimers**

ARK Microelectronics Co., Ltd. reserves the right to make change without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to ARK Microelectronics Co., Ltd's terms and conditions supplied at the time of order acknowledgement.

ARK Microelectronics Co., Ltd. warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent ARK Microelectronics Co., Ltd deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessary performed.

ARK Microelectronics Co., Ltd. does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using ARK Microelectronics Co., Ltd's components. To minimize risk, customers must provide adequate design and operating safeguards.

ARK Microelectronics Co., Ltd. does not warrant or convey any license either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in ARK Microelectronics Co., Ltd's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. ARK Microelectronics Co., Ltd is not responsible or liable for such altered documentation.

Resale of ARK Microelectronics Co., Ltd's products with statements different from or beyond the parameters stated by ARK Microelectronics Co., Ltd. for the product or service voids all express or implied warranties for the associated ARK Microelectronics Co., Ltd's product or service and is unfair and deceptive business practice. ARK Microelectronics Co., Ltd is not responsible or liable for any such statements.

Life Support Policy:

ARK Microelectronics Co., Ltd's products are not authorized for use as critical components in life devices or systems without the expressed written approval of ARK Microelectronics Co., Ltd.

As used herein:

1. Life support devices or systems are devices or systems which:
 - a. are intended for surgical implant into the human body,
 - b. support or sustain life,
 - c. whose failure to perform when properly used in accordance with instructions for used provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.