

# 350V P-Channel MOSFET

## **General Features**

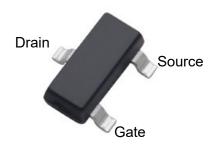
- ➤ ESD improved Capability
- Proprietary Advanced Planar Technology
- Rugged Polysilicon Gate Cell Structure
- Fast Switching Speed
- ➤ RoHS Compliant
- ➤ Halogen-free available

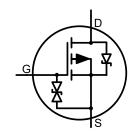
# **Applications**

- ➤ High Efficiency SMPS
- ➤ Adaptor/Charger
- Active PFC

BVDSX	RDS(ON) (Max.)	ID
-350V	30 Ω	-200mA

**SOT-23** 





**Ordering Information** 

Part Number	Package	Marking	Remark
FTZ30P35G	SOT-23	P35	Halogen Free

# **Absolute Maximum Ratings**

T<sub>A</sub>=25°C unless otherwise specified

Symbol	Parameter	FTZ30P35G	Unit
$V_{DSX}$	Drain-to-Source Voltage <sup>[1]</sup>	-350	V
$V_{DGX}$	Drain-to-Gate Voltage <sup>[1]</sup>	-350	V
$I_D$	Continuous Drain Current	-0.2	4
$I_{DM}$	Pulsed Drain Current <sup>[2]</sup>	-0.6	A
$P_{D}$	Power Dissipation	0.50	W
$V_{GS}$	Gate-to-Source Voltage	±20	V
V <sub>ESD(G-S)</sub>	Gate Source ESD IEC, C=150pF, R=330 Ω	350	V
$T_{L}$	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300	$^{\circ}$
T <sub>J</sub> and T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to 150	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

# **Thermal Characteristics**

Symbol	Parameter	FTZ30P35G	Unit
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient	250	K/W



# **Electrical Characteristics**

## **OFF** Characteristics

T<sub>A</sub> =25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	<b>Test Conditions</b>
$\mathrm{BV}_{\mathrm{DSX}}$	Drain-to-Source Breakdown Voltage	-350	1		V	$V_{GS}=0V, I_D=-250\mu A$
$\triangle BV_{DSS}/\triangle T_{J}$	Breakdown Voltage Temperature Coefficient		-0.35		V/℃	Reference to 25°C, $I_D$ =-250 $\mu$ A
				-1	μΑ	$V_{DS}$ =-350V, $V_{GS}$ = 0V
$I_{DSS}$	Drain-to-Source Leakage Current			-100	uA	$V_{DS}$ =-350V, $V_{GS}$ = 0V $T_J$ =125°C
I <sub>GSS</sub>	Gate-to-Source Leakage Current	-	-	20	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V	
				-20	uA	$V_{GS}$ =-20V, $V_{DS}$ =0V

#### **ON** Characteristics

#### $T_A = 25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	<b>Test Conditions</b>
R <sub>DS(ON)</sub>	Static Drain-to-Source On-Resistance		18	30	Ω	$V_{GS}$ =-10V, $I_D$ =-200mA <sup>[3]</sup>
V <sub>GS(TH)</sub>	Gate Threshold Voltage	-1		-3	V	$V_{GD} = 0V, I_D = -250 \mu A$

#### **Dynamic Characteristics**

#### Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Unit	<b>Test Conditions</b>
C <sub>ISS</sub>	Input Capacitance		43.39			V <sub>GS</sub> =0V
Coss	Oput Capacitance		6.94		pF	$V_{DS}$ =-25 $V$
$C_{RSS}$	Reverse Transfer Capacitance		0.84			$f=1.0MH_Z$
t <sub>d(ON)</sub>	Turn-on Delay Time		12			
$t_{rise}$	Rise Time		60		10.0	$V_{GS} = -10V \sim 0V$
$t_{d(OFF)}$	Turn-off Delay Time		136		ns	$V_{DD}$ = -25V, $I_D$ =-80mA $R_G$ = 25Ohm
t <sub>fall</sub>	Fall Time		320			

## **Source-Drain Diode Characteristics**

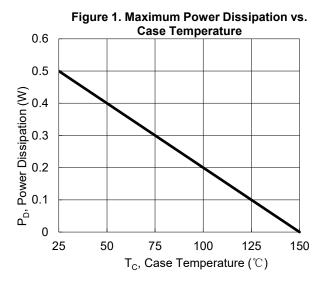
## T<sub>A</sub>=25°C unless otherwise specified

Symbol	Parameter	Min	Тур.	Max.	Units	<b>Test Conditions</b>
$V_{SD}$	Diode Forward Voltage			-1.8	V	$I_{SD}$ =-200 mA, $V_{GS}$ =0 V

#### NOTE:

- [1]  $T_J = +25^{\circ}C$  to  $+150^{\circ}C$
- [2] Repetitive rating, pulse width limited by maximum junction temperature.
- [3] Pulse width \( 380\mu s; \) duty cycle \( \le 2\%.





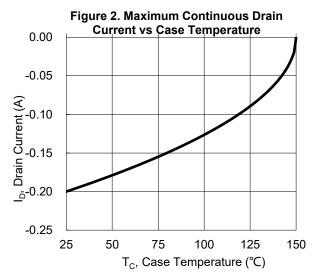
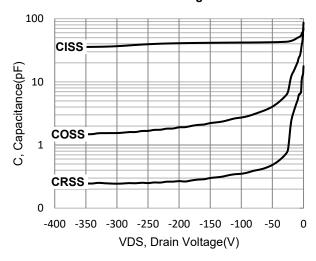
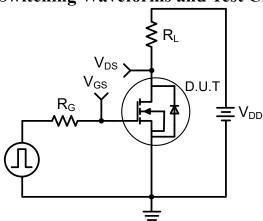


Figure 3. Typical Capacitance vs. Drain-to-Source Voltage



# **Switching Waveforms and Test Circuit**





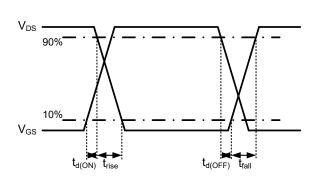
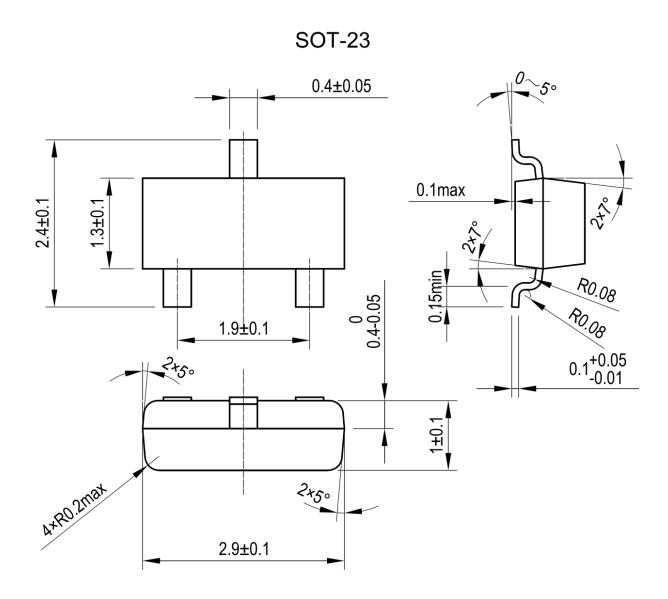


Figure 5. Resistive Switching Waveforms



# **Package Dimensions**





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