

# **Dual N-Channel 250V Enhancement Mode MOSFETs**

Marking

25N35DHVT

### **General Features**

- ⊳ Proprietary Advanced Planar Technology
- Rugged Polysilicon Gate Cell Structure ≻
- ≻ Proprietary Advanced High Vth Technology

Package

PDFN3333

RoHS Compliant ≻

Part Number

FTF25N35DHVT

 $\triangleright$ Halogen-free available

Ord	ering	Informatio	on
UI U		morman	

BV <sub>DSS</sub>	RDS(ON) (Max.)	ID
250V	25 Ω	<b>0.9</b> A



## Absolute Maximum Ratings

Absolute Maximum Ratings		$T_A=25^{\circ}C$ unless otherwise specified			
Symbol	Parameter	FTF25N35DHVT	Unit		
V <sub>DSS</sub>	Drain-to-Source Voltage <sup>[1]</sup>	250	V		
ID	Continuous Drain Current	0.9	٨		
I <sub>DM</sub>	Pulsed Drain Current <sup>[2]</sup>	3.6	А		
P <sub>D</sub>	Power Dissipation	16	W		
V <sub>GS</sub>	Gate-to-Source Voltage	±20	V		
$T_L$	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300	°C		
$T_J$ and $T_{STG}$	Operating and Storage Temperature Range	-55 to 150			

Remark

Halogen Free

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

## **Thermal Characteristics**

Symbol	Parameter	FTF25N35DHVT	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	34	K/W



## **Electrical Characteristics**

#### **OFF Characteristics**

<b>OFF Characteristics</b> $T_A = 25^{\circ}C$ unless otherwise specified						
Symbol	Parameter	Min.	Тур.	Max.	Unit	<b>Test Conditions</b>
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	250			V	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA
I <sub>DSS</sub>	Drain-to-Source Leakage Current			1	μΑ	$V_{DS}=250V$ , $V_{GS}=0V$
				100	μA	$V_{DS}=250V$ , $V_{GS}=0V$ $T_J=125$ °C
I <sub>GSS</sub>	Gate-to-Source Leakage Current			1	μA	$V_{GS}$ =+20V, $V_{DS}$ =0V
				-1		$V_{GS}$ =-20V, $V_{DS}$ =0V

### **ON Characteristics**

 $T_A = 25 \degree C$  unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
R <sub>DS(ON)</sub>	Static Drain-to-Source On-Resistance		12	25	Ω	$V_{GS}{=}10V\text{, }I_{D}{=}100mA^{[3]}$
V <sub>GS(TH)</sub>	Gate Threshold Voltage	2		5	V	$V_{GD} = 0V, I_D = 250 \mu A$
$V_{GS(TH)\_REV}$	Reverse Gate Threshold Voltage	5		10	V	$V_{GS} = 0V, I_D = -5\mu A$

#### NOTE:

[1] T<sub>J</sub>=+25°C to +150°C

[2] Repetitive rating, pulse width limited by maximum junction temperature.

[3] Pulse width $\leq$ 380µs; duty cycle $\leq$ 2%.





# **Package Dimensions**

**PDFN3333** 



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